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APPLICATION NO.	FILING DATE	. FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO
10/005,551	11/08/2001	Santanu Dutta	US018181	8019
24738 ·	7590 10/17/2005		EXAM	INER
PHILIPS ELECTRONICS NORTH AMERICA CORPORATION INTELLECTUAL PROPERTY & STANDARDS			MALZAHN, DAVID H	
	9 MCKAY DRIVE, M/S-41SJ		ART UNIT	PAPER NUMBER
SAN JOSE,			2193	

DATE MAILED: 10/17/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

1 ·			
	Application No.	Applicant(s)	
	10/005,551	DUTTA ET AL.	
Office Action Summary	Examiner	Art Unit	
•	David H. Malzahn	2193	
The MAILING DATE of this communication Period for Reply	appears on the cover sheet w	ith the correspondence address	
A SHORTENED STATUTORY PERIOD FOR REWHICHEVER IS LONGER, FROM THE MAILING - Extensions of time may be available under the provisions of 37 CF after SIX (6) MONTHS from the mailing date of this communication - If NO period for reply is specified above, the maximum statutory pe - Failure to reply within the set or extended period for reply will, by st Any reply received by the Office later than three months after the mearned patent term adjustment. See 37 CFR 1.704(b).	G DATE OF THIS COMMUN R 1.136(a). In no event, however, may a n. eriod will apply and will expire SIX (6) MO tatute, cause the application to become A	ICATION. reply be timely filed NTHS from the mailing date of this communication BANDONED (35 U.S.C. § 133).	
Status			
1) Responsive to communication(s) filed on 1	9 July 2005.		•
	This action is non-final.		
3) Since this application is in condition for allo closed in accordance with the practice und	·	•	i
Disposition of Claims			
4)⊠ Claim(s) <u>1-20</u> is/are pending in the applica	tion.		
4a) Of the above claim(s) is/are with			
5)⊠ Claim(s) <u>20</u> is/are allowed.			
6)⊠ Claim(s) <u>1-19</u> is/are rejected.			
7) Claim(s) is/are objected to.		į	
8) Claim(s) are subject to restriction ar	nd/or election requirement.		
Application Papers			
9) The specification is objected to by the Exan	niner.		
10) The drawing(s) filed on is/are: a)	accepted or b)⊡ objected to	by the Examiner.	
Applicant may not request that any objection to	the drawing(s) be held in abeya	nce. See 37 CFR 1.85(a).	
Replacement drawing sheet(s) including the col	rrection is required if the drawing	g(s) is objected to. See 37 CFR 1.121(d	I).
11)☐ The oath or declaration is objected to by the	e Examiner. Note the attache	d Office Action or form PTO-152.	
Priority under 35 U.S.C. § 119	,		
12) Acknowledgment is made of a claim for fore a) All b) Some * c) None of:	eign priority under 35 U.S.C.	§ 119(a)-(d) or (f).	
1. Certified copies of the priority docum	ents have been received.		
2. Certified copies of the priority docum		Application No	
3. Copies of the certified copies of the	priority documents have been	received in this National Stage	
application from the International Bu	reau (PCT Rule 17.2(a)).		
* See the attached detailed Office action for a	list of the certified copies no	received.	
		•	
Attachment(s)			
1) Notice of References Cited (PTO-892)	4) Interview	Summary (PTO-413)	
2) Notice of Draftsperson's Patent Drawing Review (PTO-948)	Paper No	(s)/Mail Date	
 Information Disclosure Statement(s) (PTO-1449 or PTO/SB Paper No(s)/Mail Date 	6) Other:	Informal Patent Application (PTO-152) 	

U.S. Patent and Trademark Office PTOL-326 (Rev. 7-05)

DETAILED ACTION

Claim Rejections - 35 USC § 102

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

- (b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.
- 2. Claims 1-19 are rejected under 35 U.S.C. 102(b) as being anticipated by Daniels et al (Daniels).

Daniels discloses both a circuit arrangement and a method for adding a N bit operand and a M bit operand by adding respective least-significant bit sets of the two operands to produce a least-significant bits partial sum and a carry-out (note the 8 bit adder circuit of the abstract) followed by outputting a most-significant bits sum (the most significant byte of the result, last line of the abstract) by passing out the most-significant bits of the N bit operand (the contents of the temporary register) or an offset of the most-significant bits of the N bit operand (the output of the increment/decrement network) in response to the most-significant bit of the set of least-significant bit s of the M bit operand and the carry-out (note the last line of the abstract).

Relative to applicants' remarks the "adder" corresponds to the above referenced 8 bit adder circuit and "multiplexer circuit" corresponds to Daniels' multiplexer circuit that selectively outputs the result as either the output of the increment/decrement network or the output of the temporary register.

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Claim Rejections - 35 USC § 112

3. Claims 1-19 are rejected under 35 U.S.C. 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention.

Claim 1 is mis-descriptive because N must be greater than N if the multiplexer outputs a most-significant bits partial sum because page 6 of the specification defines the most-significant bits partial sum as the (N-M) most-significant bits partial sum which means the if N = M then N-M=0 and there is not partial sum. Claim 1 is also mis-descriptive because the selection data is a function of the most-significant bit of the set of least-significant bits of the second, not first operand, note Equ. B as illustrated in Fig. 3 wherein the second operand is dzoomn and its most significant bit, namely dzoomn (15) is used as a selection bit, note Fig. 4. Claims 11, 12, 18 and 19 contain deficiencies similar to claim 1. Claims 16 and 17 are inconsistent with claim 1 because claim 1 is limited to a circuit arrangement for adding and a digital filter circuit does not fall within the scope of a circuit arrangement for adding.

Allowable Subject Matter

4. Claim 20 is allowed.

Conclusion

5. THIS ACTION IS MADE FINAL. Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

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A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of this final action.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to David H. Malzahn whose telephone number is (571) 272-3727.

The examiner can normally be reached on M-Th from 7:30 to 6:00.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Kakali Chaki, can be reached on 571-272-3719. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

DAVID H. MALZAHN PRIMARY EXAMINER